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microprocessor  
has five address  
lines [A 4 – A  
0] and eight  
data lines [D 7  
– D 0]. An input  
device A, an  
output device B,  
a ROM and a RAM

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Microprocessor  
And memory  
mapped to the  
microprocessor  
at the addresses  
as shown in  
figure. Devices  
A and B have  
four addressable

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