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Systems companies are impacted by
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methodologies offered by the semiconductor industry. New devices often bring more challenges, like increasing pin counts packaged in shrinking pin pitch ball grid arrays (BGAs). Additionally, new devices use evolving standards-based interfaces, such as DDR3, DDR4, PCI Express®

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(PCIe®) Gen3, USB 3.0, and others,
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changes made by one team member
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~~Team Design Solution - Symphony
Overview~~

The solution, of course, is the ability to zig-zag through the maze - a snake breakout. PCB Editor lets you do exactly that. So, you are in Add Connect and you can't see a straight

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Software
path, simply right-click and choose Snake Mode. Move the cursor through the channel and the traces will display as arcs.

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“ The Cadence solution reduces our

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PCB development time by 80 percent " Gisbert Thomke, Group Leader, IBM R&D Lab "With this powerful enhancement to the routing functionality, Cadence have managed to up the game and take PCB design to next level of evolution.

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