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Section 2.1:1. 8 bit 2. 8

bit 3. 8 bit 4. PSW

(Program Status Word)

is of 16 bit. 5. Necessary

(for literal value). 6. 28H

and it is kept in

accumulator. 7.

(a),(d),(g) are illegal and

for f only 0 is required

before F5H 8. (c),(d) are

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(commonly termed

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series developed by Intel in 1980 for use in embedded systems. The architect of the Intel MCS-51 instruction set was John H. Wharton. Intel's original versions were popular in the 1980s and early 1990s and enhanced binary compatible derivatives remain popular today. It is an example of a complex instruction set



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